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L21	l6 and L20	163	L21
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L17	l9 and l16	1	L17
L16	l13 or l14 or L15	401	L16
L15	katayama-kunihiro$.in.	287	L15
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L8	(ic or (integrated adj circuit)) adj2 (chip or card)	163325	L8
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Wang, R.S.C.; Shen, R.S.J.; Hsu, C.C.H.;
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☐ 2. **Neobit/sup /spl reg// - high reliable logic non-volatile memory (NVM)**
Wang, R.S.C.; Shen, R.S.J.; Hsu, C.C.H.;
Physical and Failure Analysis of Integrated Circuits, 2004. IPFA 2004. Proceedings of the 11th International Symposium on the
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Solid-State Circuits, IEEE Journal of
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Digital Object Identifier 10.1109/JSSC.2006.886523

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☐ 4. **High-Temperature, High Reliability EEPROM Design For Automotive Applications**
Walsh, J.; Scott, G.;
Conference 2006, IEEE Custom Integrated Circuits
10-13 Sept. 2006 Page(s):449 - 452
Digital Object Identifier 10.1109/CICC.2006.320818

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☐ 5. **Design and applications of ferroelectric nonvolatile SRAM and flip-flop with unlimited read/program cycles and stable recall**
Masui, S.; Yokozeki, W.; Oura, M.; Ninomiya, T.; Mukaida, K.; Takayama, Y.; Teramoto, T.;
Custom Integrated Circuits Conference, 2003. Proceedings of the IEEE 2003

21-24 Sept. 2003 Page(s):403 - 406
Digital Object Identifier 10.1109/CICC.2003.1249428

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6. **Macromodel development for a FLOTOX EEPROM**
Noren, K.V.; Ming Meng;
Electron Devices, IEEE Transactions on
Volume 45, Issue 1, Jan. 1998 Page(s):224 - 229
Digital Object Identifier 10.1109/16.658835

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7. **Contrasting single-wafer and batch processing for memory devices**
Weimer, R.A.; Eppich, D.M.; Beaman, K.L.; Powell, D.C.; Gonzalez, F.;
Semiconductor Manufacturing, IEEE Transactions on
Volume 16, Issue 2, May 2003 Page(s):138 - 146
Digital Object Identifier 10.1109/TSM.2003.810939

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8. **CMOS passive RFID transponder with read-only memory for low cost fabrication**
Jeon, W.; Melngailis, J.; Newcomb, R.W.;
SOC Conference, 2005. Proceedings. IEEE International
25-28 Sept. 2005 Page(s):181 - 184
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